IN THE SPECIFICATION

Please amend paragraph [0024] as follows:

-- [0024] FIG. 4 shows the disclosed embodiment of FIG. 2 incorporated into a computer system 600. The computer system 600 includes CPU nodes 610, I/O nodes 630, and switch matrix 620. CPU nodes 610 include the four nodes 611, 612, 613 and 614. Each of the CPU nodes 611, 612, 613 and 614 includes one or more CPUs or processors.—I/O nodes 630 include four I/O nodes 601, 602, 603 and 604. A switch fabric 620 is connected to CPU nodes 610 and to I/O nodes 630. The embodiment disclose in FIG. 2 is shown in FIG. 4 as I/O node 0. Like FIG. 2, I/O node 0 contains a parent-bridge 230. Parent-bridge 230 is attached via child-links 285 to child-bridges 260. The child-bridges 260, in turn, are connected via grand-child links 295 to buses 280. Finally, buses 280 are attached to I/O devices 290. It is contemplated, although not required, that some or all of the other I/O nodes would adopt a similar architecture shown in detail in I/O node 0. --